

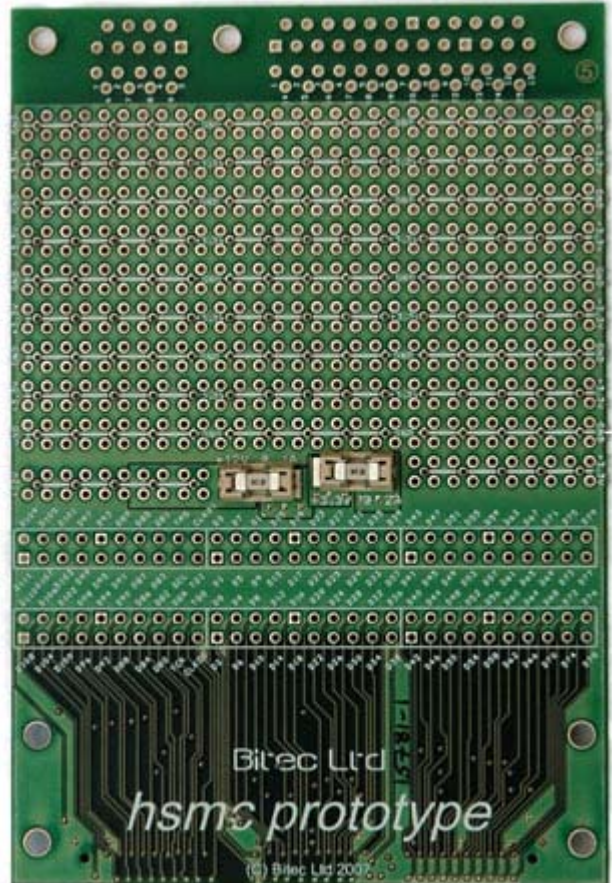
Product Brief:

HSMC Prototyping board



Features

- High quality, 4-layer, FR4 PCB with plated thru holes.
- Convenient access points to every pin on the HSMC connector.
- A white silkscreen clearly identifying all access points and voltages.
- Solder mask on both sides of the board.
- Plugs into ALTERA Stratix III and Cyclone III development kits using the HSMC interface standard.
- A dense grid of GROUND and +3.3V distributed throughout the board.
- +3.3V and GND planes throughout the board for added noise immunity.
- Access to fused +12V and +3.3V supplies.
- Break-out headers for the HSMC connector including the complete 80-available signals.



The Bitec HSMC prototype board provides an elegant and economical solution for prototyping circuits and testing them together with the latest ALTERA FPGA development kits. The board provides access to the complete set of HSMC signals via a footprint of standard 0.1" pitch headers. The HSMC power pins are accessed via fuses for added security. The main prototype matrix comprises a 0.1" grid interleaved with +3.3V and GND access points. Footprints for commonly used 25-way and 9-way d-type connectors are included on the board.

For more information www.bitec.ltd.uk

| Pin No | Function | Function | Pin No | Description |
|--------|----------|----------|--------|----------------|
| 1 | D110 | D111 | 2 | CMOS |
| 3 | D108 | D109 | 4 | CMOS |
| 5 | D106 | D107 | 6 | CMOS |
| 7 | D104 | D105 | 8 | CMOS |
| 9 | D102 | D103 | 10 | CMOS |
| 11 | D100 | D101 | 12 | CMOS |
| 13 | D98 | D99 | 14 | CMOS |
| 15 | D96 | D97 | 16 | CMOS |
| 17 | D94 | D95 | 18 | CMOS |
| 19 | D92 | D93 | 20 | CMOS |
| 21 | D90 | D91 | 22 | CMOS |
| 23 | D88 | D89 | 24 | CMOS |
| 25 | D86 | D87 | 26 | CMOS |
| 27 | D84 | D85 | 28 | CMOS |
| 29 | D82 | D83 | 30 | CMOS |
| 31 | D80 | D81 | 32 | CMOS |
| 33 | SDA | SCL | 34 | SMBUS/CMOS |
| 35 | JTAG_TCK | JTAG_TMS | 36 | JTAG/CMOS |
| 37 | JTAG_TDO | JTAG_TDI | 38 | JTAG/CMOS |
| 39 | CLKOUT0 | CLKIN0 | 40 | CMOS/CLK |
| 41 | D0 | D1 | 42 | CMOS |
| 43 | D2 | D3 | 44 | CMOS |
| 47 | D4 | D5 | 48 | CMOS |
| 49 | D6 | D7 | 50 | CMOS |
| 53 | D8 | D9 | 54 | CMOS |
| 55 | D10 | D11 | 56 | CMOS |
| 59 | D12 | D13 | 60 | CMOS |
| 61 | D14 | D15 | 62 | CMOS |
| 65 | D16 | D17 | 66 | CMOS |
| 67 | D18 | D19 | 68 | CMOS |
| 71 | D20 | D21 | 72 | CMOS |
| 73 | D22 | D23 | 74 | CMOS |
| 77 | D24 | D25 | 78 | CMOS |
| 79 | D26 | D27 | 80 | CMOS |
| 83 | D28 | D29 | 84 | CMOS |
| 85 | D30 | D31 | 86 | CMOS |
| 89 | D32 | D33 | 90 | CMOS |
| 91 | D34 | D35 | 92 | CMOS |
| 95 | D36 | D37 | 96 | LVDS CLKp/CMOS |
| 97 | D38 | D39 | 98 | LVDS CLKn/CMOS |
| 101 | D40 | D41 | 102 | CMOS |
| 103 | D42 | D43 | 104 | CMOS |
| 107 | D44 | D45 | 108 | CMOS |
| 109 | D46 | D47 | 110 | CMOS |
| 113 | D48 | D49 | 114 | CMOS |
| 115 | D50 | D51 | 116 | CMOS |
| 119 | D52 | D53 | 120 | CMOS |
| 121 | D54 | D55 | 122 | CMOS |
| 125 | D56 | D57 | 126 | CMOS |
| 127 | D58 | D59 | 128 | CMOS |
| 131 | D60 | D61 | 132 | CMOS |
| 133 | D62 | D63 | 134 | CMOS |
| 137 | D64 | D65 | 138 | CMOS |
| 139 | D66 | D67 | 140 | CMOS |
| 143 | D68 | D69 | 144 | CMOS |
| 145 | D70 | D71 | 146 | CMOS |
| 149 | D72 | D73 | 150 | CMOS |
| 151 | D74 | D75 | 152 | CMOS |
| 155 | D76 | D77 | 156 | LVDS CLKp/CMOS |
| 157 | D78 | D79 | 158 | LVDS CLKn/CMOS |

Generic single-ended HSMC pin function