

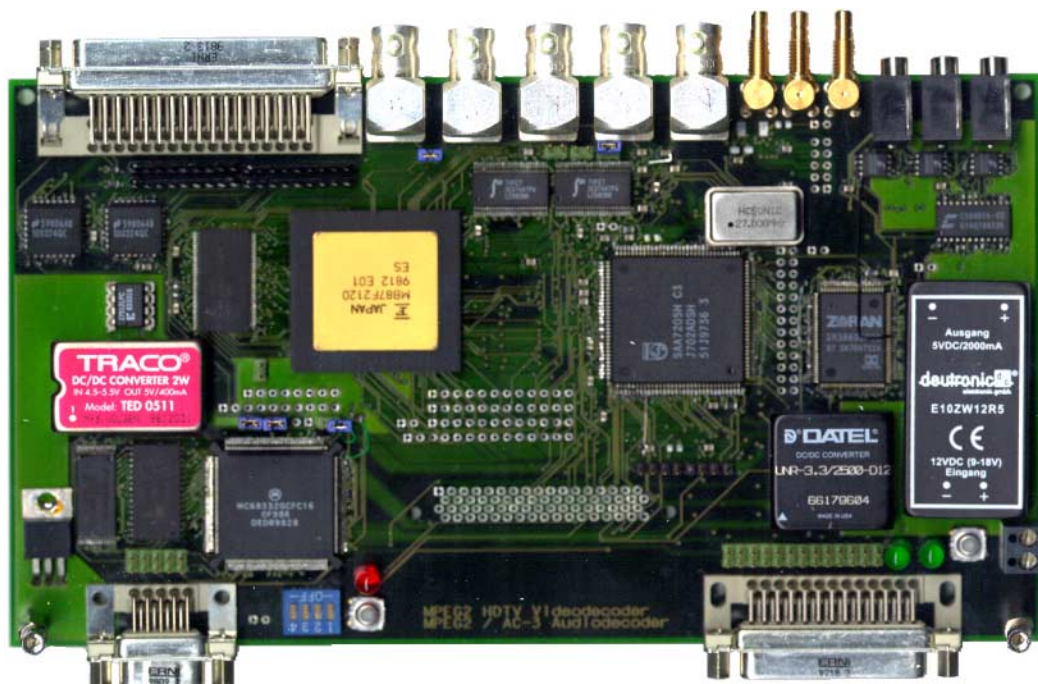


MPEG-2 High-Definition TV Decoder HiBOX 2

The MPEG-2 HDTV decoder HiBOX 2 decodes MPEG-2 HDTV video streams in realtime.

It consists of:

1. System Demultiplexer
2. Audio-Decoder
3. Video-Decoder
4. Microcontroller.



Technical Details:

- Demultiplexing of Transport-Streams up to 72 Mbps.
- Decoding of video-data up to HDTV-Resolution (1920/1080/60/2:1).
- Decoding of stereo or multi-channel surround sound (MPEG-2, AC-3 is optional).
- Flexible control of the decoders and demultiplexer using different system configurations

Features

HDTV Decoder Board containing System Demultiplexer, A/V Decoder, Microcontroller, DA-Converter and Playback facility with single power supply (12 V DC, 2 A)

Systems

- ISO/IEC 13818-1 MPEG-2 Systems compliant
- Max. bitrate up to 72 Mbps (Clock 9 MHz)
- Transport Stream Input (TSI) DVB-SPI compliant
- Clock recovery via PWM and external PLL
- Demultiplexer controlled by microcontroller

Audio

- ISO/IEC 13818-3 MPEG-2 Audio compliant
- Optional AC-3 up to 5.1 channels
- MPEG-1 Layer 2 two channels
- MPEG-2 Layer 2 two channels
- Audio outputs:
 - 3 x Analog (AAO)
 - 3 x Digital, S-PDIF/AES-EBU (DAO)

CPLD

- Data path switch, configurable to ...
 - route TSI to DEMUX and DEMUX output to video decoder or
 - route TSI to DEMUX and video decoder or
 - route PCMCIA to video decoder
- PCMCIA playback system
 - PCMCIA FLASH memory card reader
 - Single-shot or wrap-around mode
 - TS and ES/PES interface protocol generation (Strobe signal)

Video

- ISO/IEC 13818-2 MPEG-2 MP@HL compliant
- Supports TS, PES and ES input
- Supports all 18 ATSC formats
- Supports 50 and 60 Hz world in both decoding and display processing
- Interlace or progressive video output
- Max. Pixel clock 81 MHz
- Integrated SDRAM controller
- Highly configurable display controller
- No runtime controlling needed
- Video outputs:
 - Analog: R, G, B, HSync, VSync (AVO)
 - Digital, ECL: YC C, compliant (DVO)
 - Digital, LVDS: (PDP-VO)

Microcontroller

- DOS-like operating system BOS
- FLASH RAM for nonvolatile data and RAM for run-time data
- Host interface (RS232)



Contact

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